

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

FORD B. GRIGG
KENNETH N. GLOVER

Serial No.

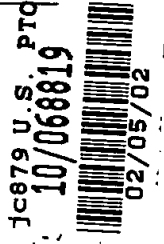
ART UNIT:

Filing Date: February 4, 2002

EXAMINER: #2

Title: MASKING FLUX FOR SEMICONDUCTOR COMPONENTS

Docket No. 01-0371



INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.56
February 4, 2002

BOX PATENT APPLICATION
Commissioner of Patents
Washington, DC 20231

Sir:

The citations listed on the accompanying Form PTO/SB/08, copies attached, may be material to the examination of the subject application and are therefore submitted in compliance with the duty of disclosure defined in 37 CFR §§1.56, 1.97 and 1.98.

This Information Disclosure Statement under 37 CFR §§1.56, 1.97 and 1.98 is not to be construed as a representation that a search has been made, that additional material to the examination of this application does not exist, or that any one or more of these citations constitutes prior art under 35 USC §102.

It is requested that the above citations be made of record, if appropriate, in the prosecution of this application.

Respectfully submitted:

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Address" Service under 37 CFR 1.10 on the date indicated above and is addressed to the BOX PATENT APPLICATION, Commissioner of Patents, Washington, D.C. 20231.

February 4, 2002
Date of Signature

Stephen A. Gratton, Attorney for Applicants